

#### REMARKS

Applicants respectfully request favorable reconsideration of this application, as amended.

Claims 7-10 and 13 are amended herein to improve clarity and expression, including correction of the alleged informalities of Claims 7 and 12. Claims 1-6 and 14-28 have been canceled without prejudice or disclaimer. Claims 11 and 12 are canceled herein without prejudice or disclaimer, and new Claims 29-55 are added herein.

In the Office Action, Claims 7-13 were rejected under 35 U.S.C. § 103 over Sun in combination with Niwata. Without acceding to the rejection under 35 U.S.C. § 103, Claim 7 has been amended to recite, *inter alia*, a resistance connected between a first one of the plurality of adapter external terminals and at least one of the adapter internal terminals, with a supply voltage or ground voltage being applied to the resistance to cause a value to be present at one of the plurality of external terminals of the IC card of the first standard, wherein the value is between said supply voltage and ground. Neither Sun nor Niwata teaches or suggests this feature of Claim 7.

New Claims 29-55 have been added herein to protect further aspects of Applicants' invention. New Claims 29-31 are believed to be patentable based on their dependence from Claim 7 in addition to the features recited in Claims 29-31.

New independent Claims 32 and 48 recite, *inter alia*, a semiconductor chip connected between a first one of the plurality of adapter external terminals and at least one of the adapter internal terminals and constructed to cause a value to be present at one of the plurality of external terminals of the IC card of the first standard. Neither Sun nor Niwata teaches or suggests this feature of Claims 32 and 48. New Claims 33-39 and 49-55 are believed to be patentable at least for their dependence from Claims 32 and 48, respectively, in addition to the features recited in Claims 33-39 and 49-55.

New independent Claim 40 recites, *inter alia*, a resistance connected between a first one of the plurality of adapter external terminals and at least one of the adapter internal terminals, with a supply voltage or ground voltage being applied to the resistance to cause a value to be present at one of the plurality of external terminals of the IC card of the first standard, wherein the value is between said supply voltage and ground. As the applied references neither teach nor suggest this feature, Claims 40-47 are also believed to be patentable.

In view of the amendments and remarks presented herein, Applicant respectfully requests an early Notice of Allowance.

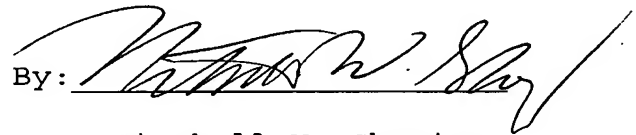
The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been requested separately, such extension is hereby requested.

Respectfully submitted,

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